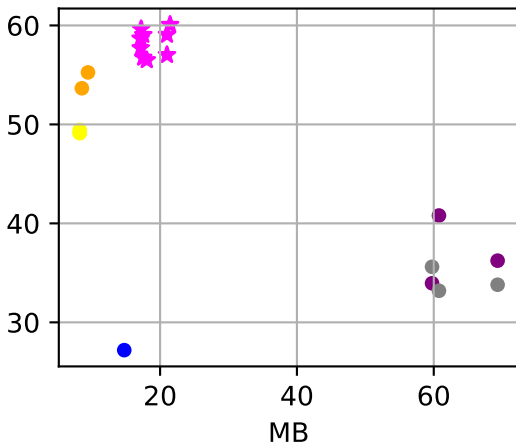
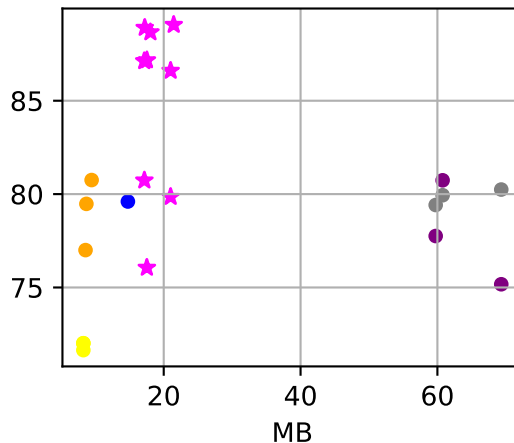


Comparison of Trade-off Between Computational Cost and Accuracy

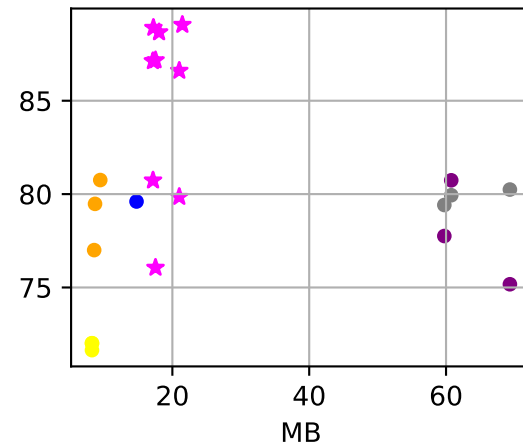
Memory / CoLA



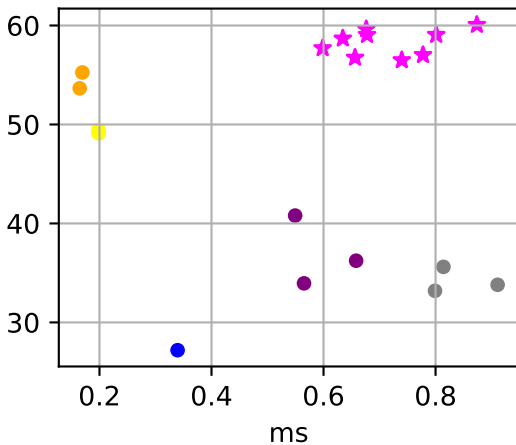
Memory / MRPC



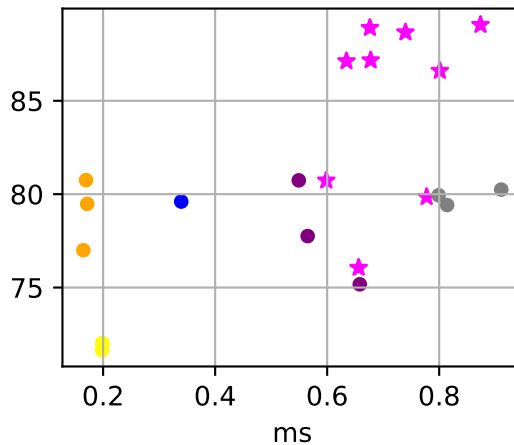
Memory / MNLI



Latency / CoLA



Latency / MRPC



Latency / MNLI

